| | Application No. | Applicant(s) | |
|--|--|---------------------------------|--|
| Ma4: E A H L *1*4 | 10/626,701 | BROOKS ET AL. | |
| Notice of Allowability | Examiner | Art Unit | |
| | Daniel D. Chang | 2819 | |
| The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308. | | | |
| 1. This communication is responsive to <u>Amendment filed 5/9/2005</u> . | | | |
| 2. The allowed claim(s) is/are <u>15-34</u> . | | | |
| 3. The drawings filed on 18 January 2005 are accepted by the Examiner. | | | |
| 4. | | | |
| Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 7/25/03 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material | 5. Notice of Informal Pa 6. Interview Summary Paper No./Mail Date 8), 7. Examiner's Amendm 8. Examiner's Stateme 9. Other | (PTO-413), e nent/Comment | |
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Acknowledgement

Receipt is acknowledged of the Amendment filed May 9, 2005.

Reasons for Allowance

Claims 15-34 are allowable over the prior art of record.

The following is an examiner's statement of reasons for allowance: the best prior art of record, Hamasaki et al., taken alone or in combination of other references, does not teach or fairly suggest a circuit comprising, among other things, a sampling circuit capable of storing a charge of a sample having a switch (claims 15 and 34); and the buffer includes a source-follower transistor adapted to generate an output signal from an input signal, a replica transistor adapted to generate a replica signal from the input signal, and a level shifting circuit that provides a level-shifted replica signal at a terminal of the source-follower transistor (claim 18), as set forth in the claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daniel D. Chang whose telephone number is (571) 272-1801. The examiner can normally be reached on Monday through Friday.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael J. Tokar can be reached on (571) 272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Daniel D. Chang

Primary Examiner

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DANIEL CHANG PRIMARY EXAMINER